

Fig.1

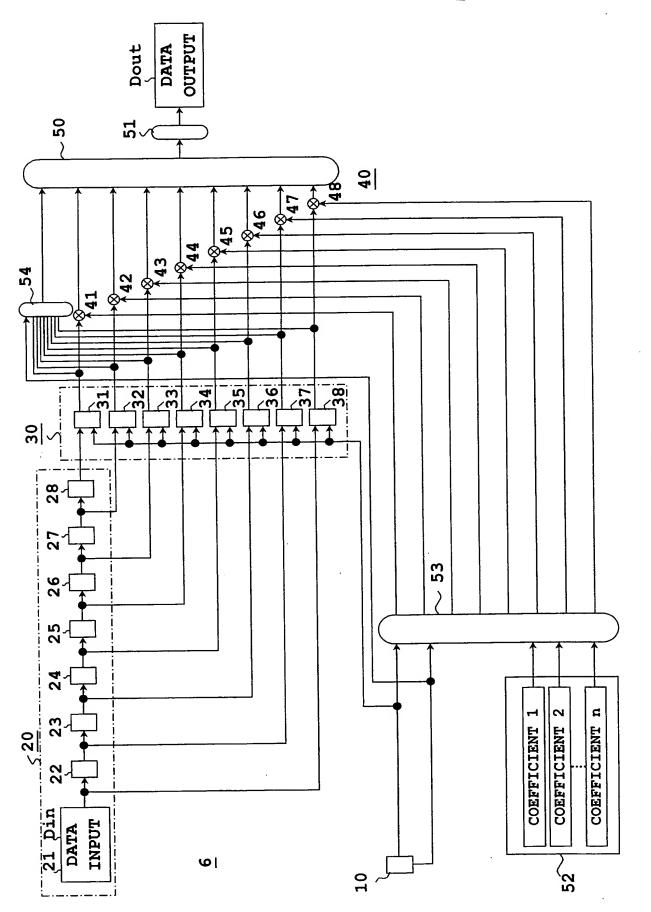
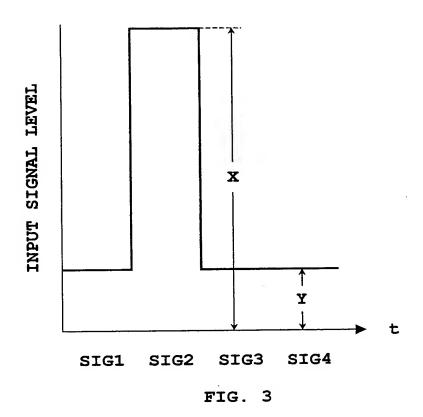
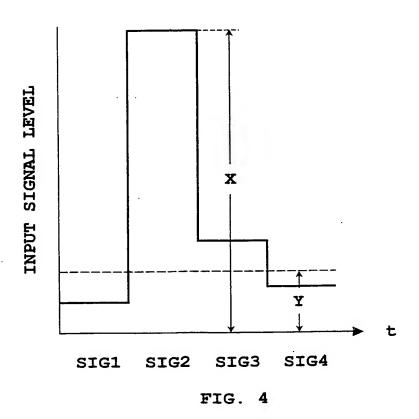
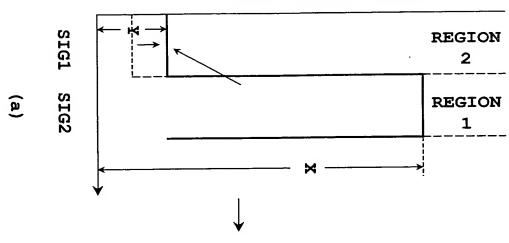


Fig. 2

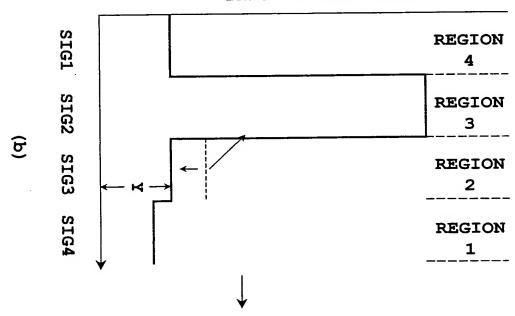






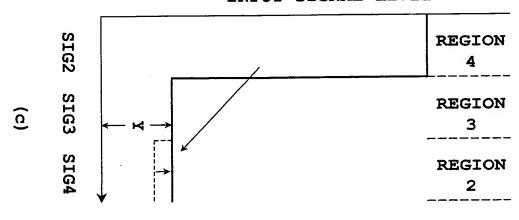


INPUT SIGNAL LEVEL



G

INPUT SIGNAL LEVEL



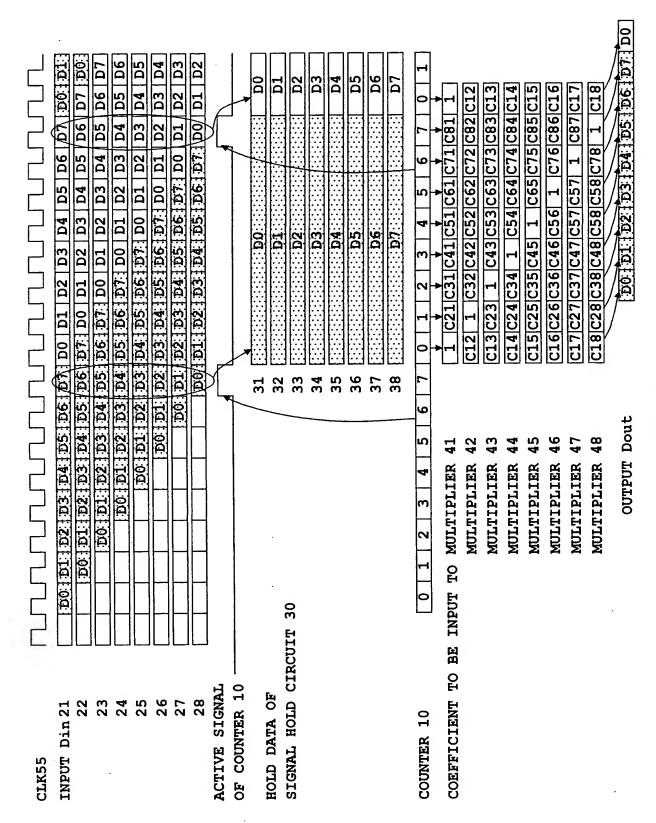


FIG. 6

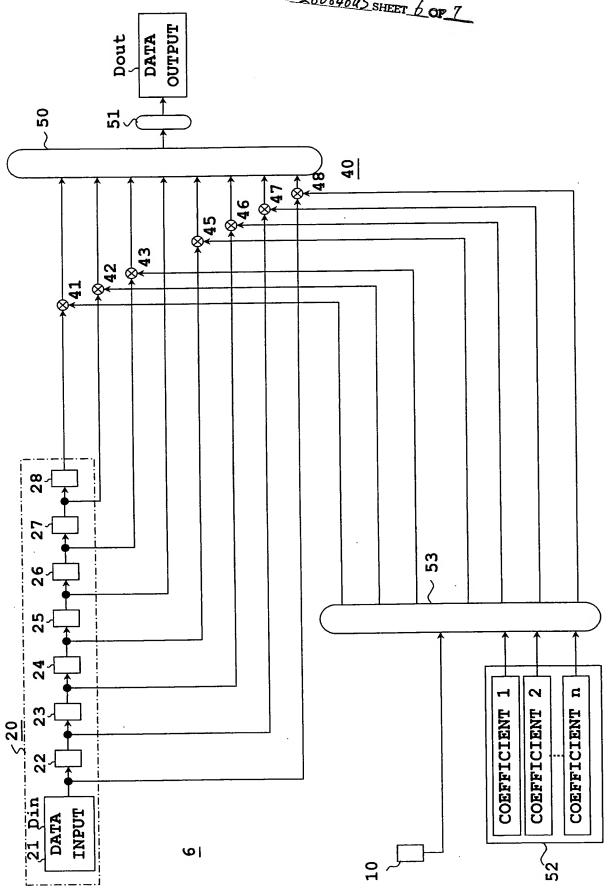


Fig. 7

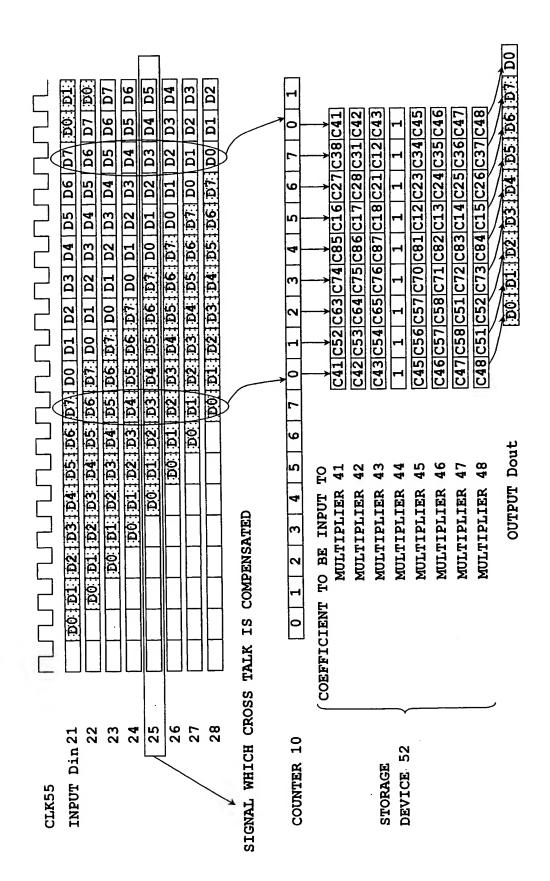


FIG. 8